

AMENDMENTS TO THE CLAIMS

Claims 1-21 (Cancelled)

22. (Currently amended) A transmission gate, comprising:
a switch circuit including an input that is coupled to an input node, an output that is coupled to an output node, and a control input that is coupled to a control input node;
a boost circuit that is arranged to provide a boost voltage at a boost node responsive to a supply voltage;
a current source circuit that is coupled between the boost node and the control input node, wherein the current source circuit is arranged to provide a bias current during an on condition; and
a constant voltage difference circuit that is coupled to the control input node, wherein the constant voltage difference circuit is arranged to provide a control input voltage at the control input node such that a voltage difference between the control input voltage and an input voltage at the input node is substantially constant during the on condition~~The transmission gate of Claim 21, wherein~~
the constant voltage difference circuit is coupled between the input node and the control input node, and wherein the constant voltage difference circuit includes at least one of a diode or ~~and~~ a transistor.

23. (Currently amended) A transmission gate, comprising:
a switch circuit including an input that is coupled to an input node, an output that is coupled to an output node, and a control input that is coupled to a control input node;
a boost circuit that is arranged to provide a boost voltage at a boost node responsive to a supply voltage;
a current source circuit that is coupled between the boost node and the control input node, wherein the current source circuit is arranged to provide a bias current during an on condition; and
a constant voltage difference circuit that is coupled to the control input node, wherein the constant voltage difference circuit is arranged to provide a control input voltage at the control input node

such that a voltage difference between the control input voltage and an input voltage at the input node is substantially constant during the on condition~~The transmission gate of Claim 21~~, wherein the constant voltage difference circuit includes a plurality of transistors, and wherein the constant voltage difference circuit is arranged such that the substantially constant voltage difference is substantially equal to the sum of the gate-to-source voltages of each of the plurality of transistors.

24. (Cancelled)

25. (Currently amended) A transmission gate, comprising:

a switch circuit including an input that is coupled to an input node, an output that is coupled to an output node, and a control input that is coupled to a control input node;

a boost circuit that is arranged to provide a boost voltage at a boost node responsive to a supply voltage;

a current source circuit that is coupled between the boost node and the control input node, wherein the current source circuit is arranged to provide a bias current during an on condition; and a constant voltage difference circuit that is coupled to the control input node, wherein the constant voltage difference circuit is arranged to provide a control input voltage at the control input node such that a voltage difference between the control input voltage and an input voltage at the input node is substantially constant during the on condition~~The transmission gate of Claim 21~~, wherein:

the boost circuit is a charge pump circuit that is arranged to provide the boost voltage such that the boost voltage is greater than the supply voltage; and

wherein the current source circuit is a current mirror circuit that is arranged to provide the bias current such that the bias current is substantially constant.

26. (Currently amended) A transmission gate, comprising:

a switch circuit including an input that is coupled to an input node, an output that is coupled to an output node, and a control input that is coupled to a control input node;

a boost circuit that is arranged to provide a boost voltage at a boost node responsive to a supply voltage;

a current source circuit that is coupled between the boost node and the control input node, wherein the current source circuit is arranged to provide a bias current during an on condition; and a constant voltage difference circuit that is coupled to the control input node, wherein the constant voltage difference circuit is arranged to provide a control input voltage at the control input node such that a voltage difference between the control input voltage and an input voltage at the input node is substantially constant during the on condition~~The transmission gate of Claim 21~~, wherein the constant voltage difference circuit is arranged to provide the control input voltage such that the control input voltage tracks the input voltage such that the voltage difference between the control input voltage and the input voltage is substantially constant, but such that the voltage difference varies slightly over temperature and process.

27. (Previously Presented) The transmission gate of Claim 26, wherein the constant voltage difference circuit is arranged to provide the control input voltage such that the voltage difference between the control input voltage and the input voltage varies over temperature and process such that an on-resistance of the switch circuit between the input node and the output node is substantially constant over temperature and process.

28. (Currently amended) A transmission gate, comprising:
a switch circuit including an input that is coupled to an input node, an output that is coupled to an output node, and a control input that is coupled to a control input node;
a boost circuit that is arranged to provide a boost voltage at a boost node responsive to a supply voltage;
a current source circuit that is coupled between the boost node and the control input node, wherein the current source circuit is arranged to provide a bias current during an on condition; and a constant voltage difference circuit that is coupled to the control input node, wherein the constant voltage difference circuit is arranged to provide a control input voltage at the control input node such that a voltage difference between the control input voltage and an input voltage at the input node is substantially constant during the on condition~~The transmission gate of Claim 21~~, wherein the constant voltage difference circuit includes:

control input node such that a voltage difference between the control input voltage and an input voltage at the input node is substantially constant during an on condition of the first transmission gate, and wherein the constant voltage difference circuit includes at least one of a first transistor or ~~and~~ a first diode.

34. (Previously Presented) The circuit of Claim 33, further comprising:
a second switch circuit including an input that is coupled to the input node, an output that is coupled to a second output node, and a control input that is coupled to a control input node.
35. (Previously Presented) The circuit of Claim 33, further comprising:
a second transmission gate, including:
a second switch circuit including an input that is coupled to a second input node, an output that is coupled to the output node, and a control input that is coupled to a second control input node; and
a second constant voltage difference circuit that is coupled to the second control input node, wherein the second constant voltage difference circuit is arranged to provide a second control input voltage at the second control input node such that a voltage difference between the second control input voltage and a second input voltage at the second input node is substantially constant during an on condition of the second transmission gate.
36. (Previously Presented) The circuit of Claim 35, wherein the second input voltage is a calibration voltage, the on condition of the second transmission gate occurs during a calibration phase, and an on condition of the first transmission gate occurs during a normal operation.
37. (Previously Presented) The circuit of Claim 36, wherein:
the first transmission gate further includes:
a boost circuit that is arranged to provide a boost voltage at a boost node responsive to a supply voltage, such that the boost voltage is greater than the supply voltage;

